

AT32Fxx TMR Overflow Interrupt

Introduction

This sample code demonstrates how to use AT32F4xx series TMR overflow interrupt.

Note: This sample code is written based on Artery's V2.x.x BSP. For other versions of BSP, users should pay attention to the differences in use.

Applicable products:

Product series	AT32F4xx
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List of major peripherals used:

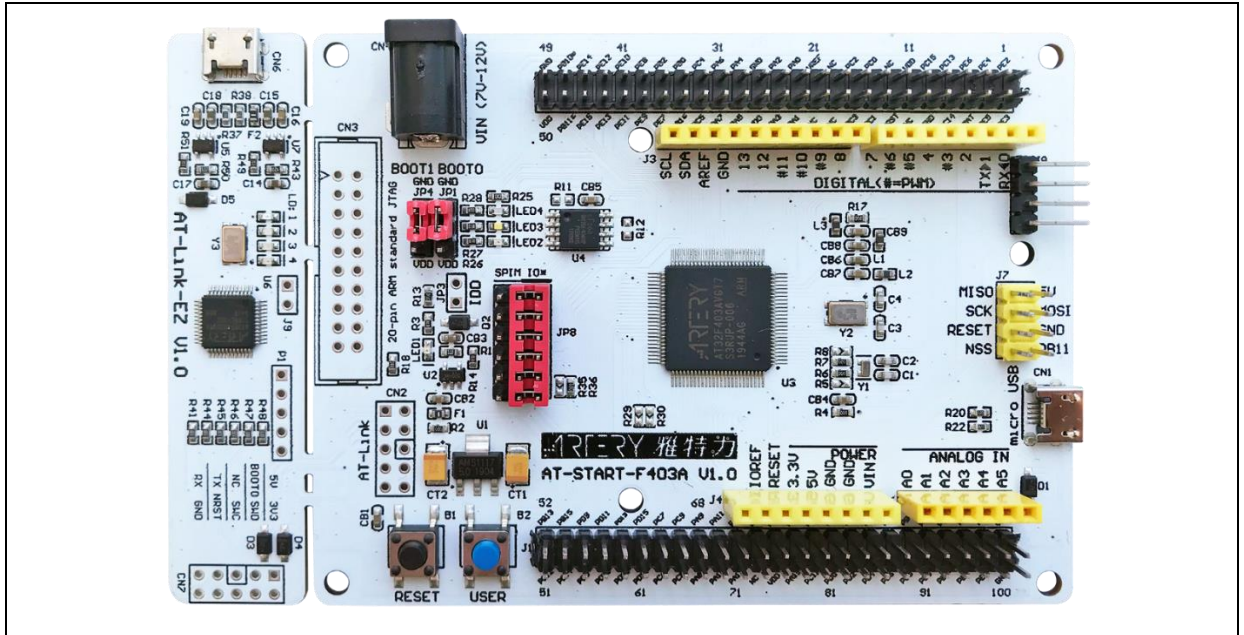
Peripherals	TMR
	GPIO

1 Quick start

1.1 Hardware resources

- 1) AT-START-F403A V1.0 evaluation board (select evaluation board according to the corresponding MCU series)
- 2) LED3

Figure 1. AT-START-F403A V1.0 evaluation board



1.2 Software resources

- 1) Source code
 - tmr_base

Note: All of projects are built based on Keil 5. For the need to run in other compiling environments, user can make simple adjustments according to AT32xxx_Firmware_Library_V2.x.x\project\at_start_xxx\templates.

1.3 Example case

- 1) Open tmr_base, compile and download it to the evaluation board AT-START-F403A
- 2) AT-START-F403A V1.0 evaluation board is used here, so we select AT32F403A project
- 3) For users to view test result, TMR overflow interrupt is programmed in the program. LED3 will toggle upon an overflow interrupt. If LED3 blinks every other 1 second, it means normal operation.

2 Revision history

Table 1. Document revision history

Date	Revision	Changes
2021.12.07	2.0.0	Initial release

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